





FIG. 4 is a schematic diagram of a semiconductor device layout. It shows a central channel 3 between two regions 26a and 26c. Region 26a contains structures 11, 12, 27, 28, 27a, and 27b. Region 26c contains structures 13, 14, 13a, 14a, 25b, and 26c. Brackets group these into 24a, 24b, and 24c. A dimension L3 is indicated between the central structures.

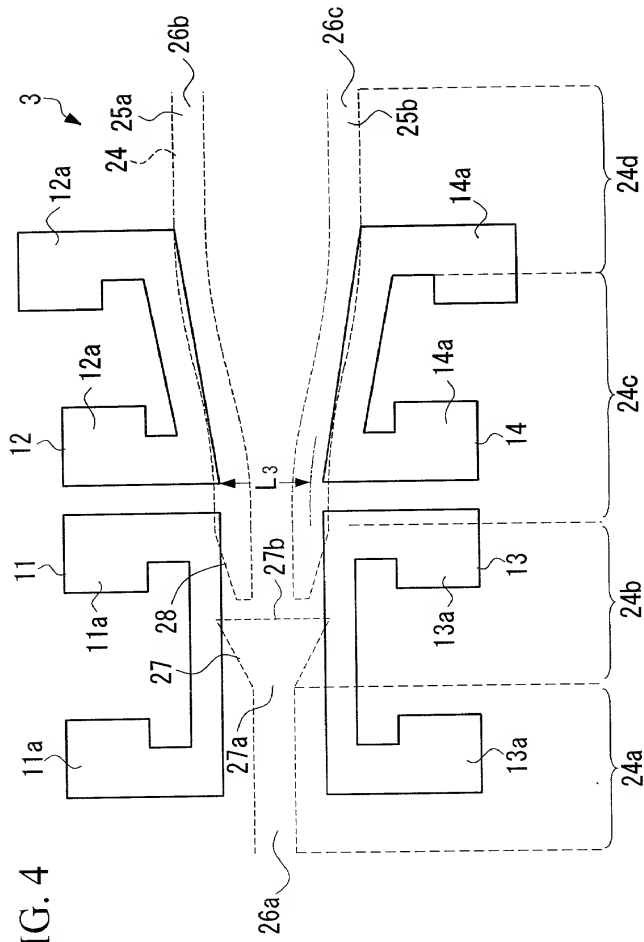


FIG. 5

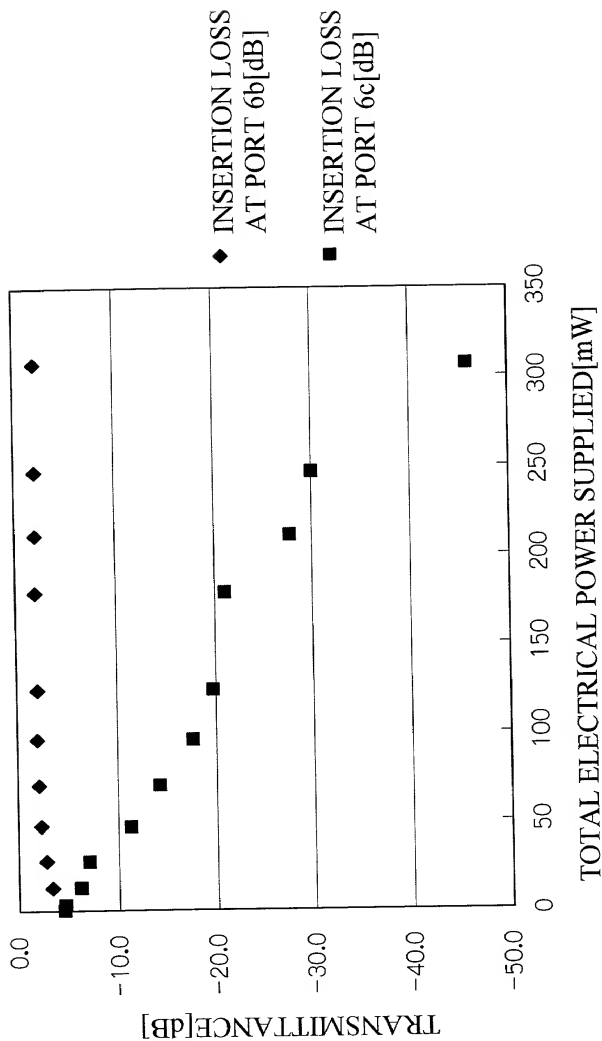
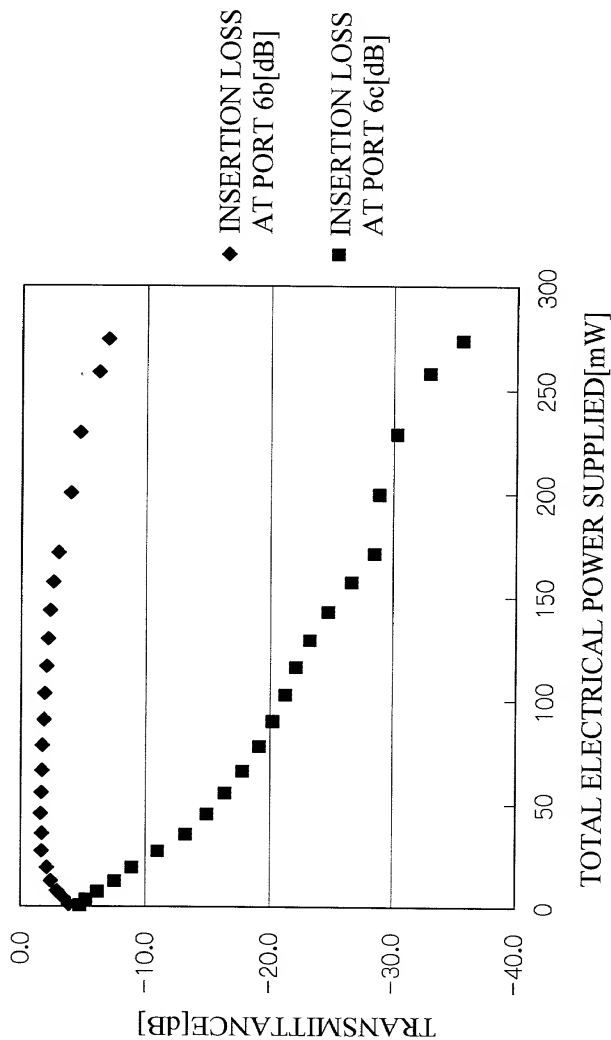


FIG. 6



[illegible]